

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: H. MATSUOKA et al
Serial No. 09/592,648
Filed: June 13, 2000

Group Art: 2814
Examiner: D. Nguyen
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND A METHOD OF MANUFACTURE THEREOF

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 31, 2003

Sir:

Prior to examination, please amend the above application
as follows.